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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/815,695	04/02/2004	In-Young Chung	8947-000073/US	9739
30593 7	590 02/14/2006		EXAMINER	
	DICKEY & PIERCE,	TON, MY TRANG		
P.O. BOX 8910 RESTON, VA	•		ART UNIT	PAPER NUMBER
122131, 11			2816	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	10/815,695	CHUNG, IN-YOUNG	
Office Action Summary	Examiner	Art Unit	
	My-Trang N. Ton	2816	
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w. - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tir rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. ED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 2a) This action is FINAL. 2b) This 3) Since this application is in condition for allowan closed in accordance with the practice under E Disposition of Claims	action is non-final. nce except for formal matters, pro		
 4) Claim(s) 1-22 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-5 and 7-22 is/are rejected. 7) Claim(s) 6 is/are objected to. 8) Claim(s) are subject to restriction and/or 	vn from consideration.		
Application Papers			
9)☐ The specification is objected to by the Examiner 10)☒ The drawing(s) filed on 02 April 2004 is/are: a)[Applicant may not request that any objection to the off Replacement drawing sheet(s) including the correction of the off the oath or declaration is objected to by the Examiner.	☑ accepted or b)☐ objected to drawing(s) be held in abeyance. Set on is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive (PCT Rule 17.2(a)).	on No ed in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 7/7/05.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	(PTO-413)	

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DETAILED ACTION

Claim Rejections - 35 USC § 112

Claims 4, 7-12, 16-22 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 4, it is unclear as to whether "a power down signal" recited in line 2 is additional limitation "a precharging control signal" as cited in line 3, claim 1.

Claim 7 recites the limitation "the second output driver" in line 11. There is insufficient antecedent basis for this limitation in the claim. Moreover, it is unclear as to whether "a first data bit" recited in last line is a part of "associated data bits" as cited in line 3.

Claims 10,16 and 20 are similarly rejected as claim 4 regarding "a power down signal".

Claim 17 is similarly rejected as claim 7 regarding "a first data bit" recited in last line.

In single claim 21, which claims both an apparatus and the method steps of using the apparatus is indefinite under 35 U.S.C. 112, second paragraph. In Ex parte Lyell, 17 USPQ2d 1548 (Bd. Pat. App. & Inter. 1990). Claim 21 directed to a system comprising a gate circuit and the method steps of using it was held to be ambiguous and properly rejected under 35 U.S.C. 112, second paragraph.

Claim 22 is similarly rejected as claim 21. Moreover, claim 22 is also similarly rejected as claim 7 regarding the limitation "the second output driver" (line 7).

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Claims 8-9, 11-12, 18-19 are rendered indefinite by the deficiencies of claims 7 and 17.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5, 13-16 and 21 are rejected under 35 U.S.C. 102(b) as being anticipated by Yau et al (U.S Patent No. 6,498,520).

Yau et al disclose in Figs. 1-14 a system for minimizing the effect of clock skew including:

a gate circuit (200" – 400") for inputting a precharging control signal (precharge enable) and for generating an output driving signal (output of 200" and 400") in response to the control signal (precharge enable) before a data bit (select<0>, select<1>) is input; and

an output driver (MP, MN1) connected to a data channel (channel connected to VDD) and responsive to the output driving signal (the output of 200" and 400") for precharging the data channel to a voltage level before the data bit (select<0>, select<1>) is input as recited in claim 1.

Regarding claim 2: the control signal (precharge-enable) is enabled during a time period before the data bit (select<0>, select<1>) is input.

The limitation recited in claim 3 is inherent seen in Fig. 14.

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Regarding claim 4: the gate circuit (200"-400") does not generate the output driving signal (the output of 200" and 400") upon receiving a power down signal (switch enable) associated with a power down logic state.

Regarding claim 5: the output driver includes first and second transistors (MP, MN1) connected serially between the data channel (the channel connected to VDD) and a ground voltage (GND).

The method recited in claims 13-16 and 21 are similarly rejected as claims 1-5.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

My-Trang N. Ton Primary Examiner Art Unit 2816